

**Amendments to th Specification:**

**Page 1**, delete line 1:-- ~~Description~~ --

amend the title in lines 3 and 4 as follows:

-- ~~Switching converter and method for driving a switch  
in a switching converter~~ SWITCHING CONVERTER AND  
METHOD FOR DRIVING A SWITCH IN A SWITCHING CONVERTER--

between lines 4 and 6, insert

-- Background of the Invention:

Field of the Invention: --.

**Page 3**, between lines 11 and 13, insert

-- Summary of the Invention: --.

Delete the paragraph from lines 22-24:

-- ~~These aims are achieved by means of a switching  
converter in accordance with the features of claim 1  
and a method in accordance with the features of claim  
11.~~ --

**Pag 6**, replace the paragraph from lines 19-20 with the  
following amended paragraph:

-- The present invention is explained in more detail below using exemplary embodiments ~~of referenced figures in which~~ shown in the figures. --

between lines 20 and 22 insert

-- Brief Description of the Drawings: --;

**Page 7,** between lines 16 and 18, insert

-- Description of the Preferred Embodiments: --.

**Page 9,** lines 1-15, replace the paragraph with the following material:

-- The control signal RS is generated by a controller arrangement RA1 in a manner dependent on the output voltage Uout. For this purpose, an output voltage signal US is fed to the controller arrangement RA1. In order to provide this output voltage signal US, provision is made of an optocoupler OK with a light-emitting diode and a ~~photoresist PT,~~ the phototransistor PT. The light-emitting diode LED ~~being~~ is connected in series with a resistor R1 between the output terminals AK1, AK2 of the rectifier arrangement GL1. The collector-emitter path of the phototransistor PT is connected in series with a resistor R2 between a supply potential V2 and reference-ground potential M. The output voltage signal US,

proportional to the output voltage  $U_{out}$ , represents a voltage with respect to reference-ground potential M which can be tapped off at the collector of the phototransistor PT. --

**Pages 20-21**, delete the material on both of these pages

~~List of reference symbols~~

~~RA1 Controller arrangement~~  
~~RS Control signal~~  
~~AS1 Drive circuit~~  
~~CLK Clock signal~~  
~~K1 Clocked comparator arrangement~~  
~~Vref Reference voltage signal~~  
~~AI Drive pulses~~  
~~Uin Input voltage~~  
~~M Reference-ground potential~~  
~~T1, T2, T3 Transistors~~  
~~L1 Primary coil~~  
~~L2 Secondary coil~~  
~~TR Transformer~~  
~~GL1 Rectifier arrangement~~  
~~D1 Diode~~  
~~C1 Capacitor~~  
~~AK1, AK2 Output terminals~~  
~~RL Load~~

~~U<sub>out</sub>~~ ————— ~~Output voltage~~

~~R<sub>1</sub>, R<sub>2</sub>~~ ————— ~~Resistors~~

~~V<sub>2</sub>~~ ————— ~~Supply potential~~

~~PT~~ ————— ~~Photo transistor~~

~~LED~~ ————— ~~Light emitting diode~~

~~OK~~ ————— ~~Opto coupler~~

~~K~~ ————— ~~Comparator~~

~~FF~~ ————— ~~Flip flop~~

~~IF~~ ————— ~~Pulse shaper~~

~~D~~ ————— ~~Delay element~~

~~Q<sub>out</sub>~~ ————— ~~Output signal of the flip flop~~

~~S<sub>1</sub>, S<sub>2</sub>~~ ————— ~~Switches~~

~~C<sub>2</sub>~~ ————— ~~Capacitor~~

~~OPV~~ ————— ~~Operational amplifier~~

~~V<sub>ref</sub>~~ ————— ~~Reference voltage~~

~~10~~ ————— ~~Sampling unit~~

~~12~~ ————— ~~Subtraction unit~~

~~14~~ ————— ~~Summer~~

~~RA<sub>2</sub>~~ ————— ~~Controller arrangement~~

~~AS<sub>2</sub>~~ ————— ~~Drive circuit~~

~~18~~ ————— ~~Pulse shaper~~

~~16~~ ————— ~~Comparator arrangement~~

~~PW<sub>1</sub>, PW<sub>2</sub>~~ ————— ~~Level converters~~

~~L<sub>3</sub>~~ ————— ~~Coil~~

~~C<sub>3</sub>~~ ————— ~~Capacitor~~

~~GL2~~ ~~Rectifier arrangement~~

**Page 22**, amend the top line as follows:

-- ~~Patent Claims~~ I claim: --.

After page 25, please add the Abstract, which is provided on the following page: